



TekExpress HDM

Source Test Report

Setup Information			
DUT ID	DUT001	Scope Model	DPO71604C
Date/Time	2019-02-23 09:18:16	Scope Serial Number	B110695
Device Type	HDM Physical Layer Solution	SPC, FactoryCalibration	PASS;PASS
TekExpress Version	HDM:3.0.1.23 Framework:3.0.2.96	Scope F/W Version	7.6.0 Build 7
Spec Version	CTS 2.0	DPOJET Version	7.1.2.38
Overall Compliance Mode	Yes	Probe1 Model	P7313SMA
Execution Mode	Live	Probe1 Serial Number	B022381
Overall Execution Time	0:01:09	Probe2 Model	P7313SMA
Overall Test Result	Pass	Probe2 Serial Number	B022380
		Probe3 Model	P7313SMA
		Probe3 Serial Number	B020646
		Probe4 Model	P7313SMA
		Probe4 Serial Number	B021133
DUT COMMENT:	General Comment – HDM2.0 Source		

Test Name Summary Table		
Test Name	Result	Execution Time
HF1-6- Clock Duty Cycle and Clock Rate	Pass	0:01:05

HF1-6- Clock Duty Cycle and Clock Rate										
Lane Name	Measurement Details	Measured Value	Units	TBit	Data Rate	Test Result	Margin	Low Limit	High Limit	Comments
Clock	Maximum Duty Cycle	50.23	%	168.5187 ps	5.94 Gbps	Pass	-9.77	-	60.00	
Clock	Minimum Duty Cycle	49.66	%	168.5187 ps	5.94 Gbps	Pass	9.66	40.00	-	
Clock	Clock Rate	148.35	MHz	168.5187 ps	5.94 Gbps	Pass	63.35 & 1.65	85	150	
COMMENTS										

[Back to Summary Table](#)

HF1-6- Clock Duty Cycle and Clock Rate

Waveform Plot for Clock Duty Cycle and Clock Rate

This figure is a placeholder for a waveform plot. The title is "HF1-6- Clock Duty Cycle and Clock Rate". The plot area is currently empty, showing only a white background within a black border. The caption below the plot is "Waveform Plot for Clock Duty Cycle and Clock Rate".

