

Revision History:

Rev -01 (02/17/17)

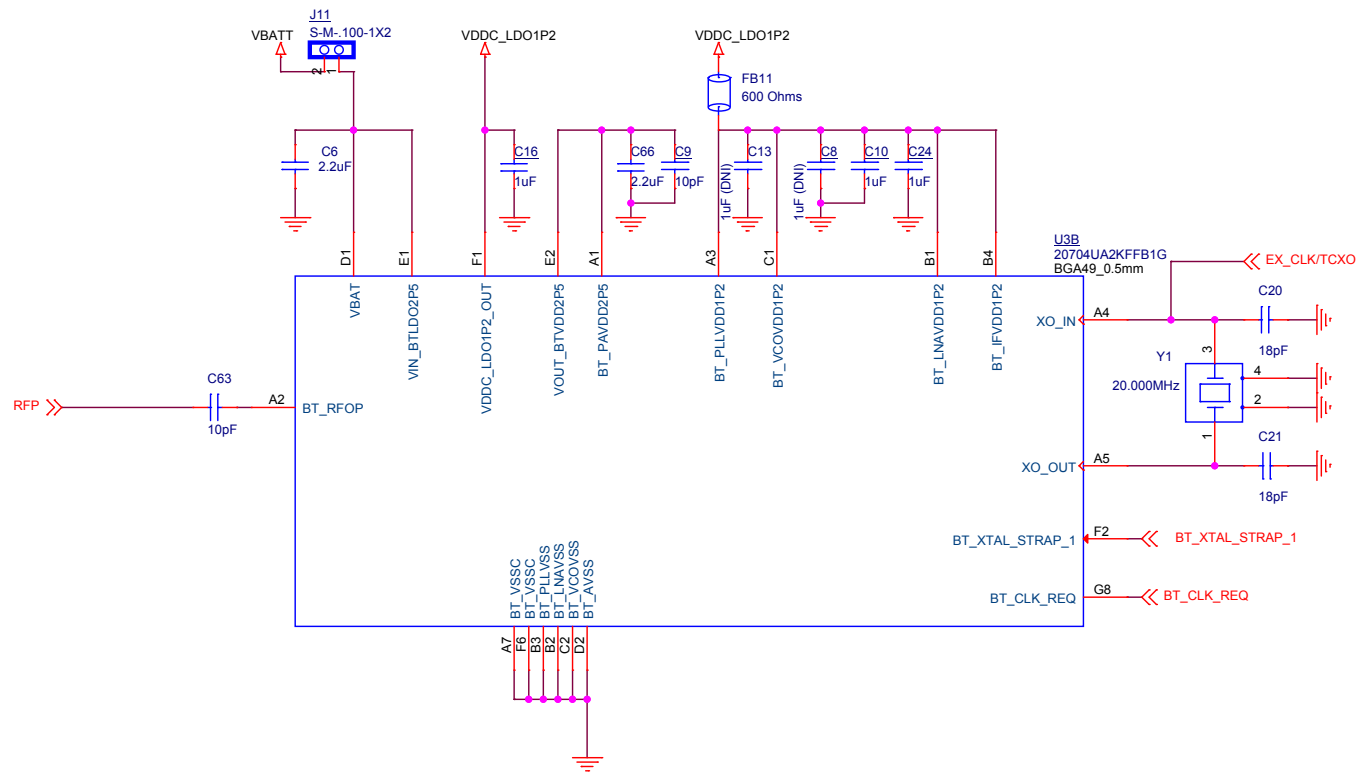
1) Initial release

PAGE1: COVER PAGE  
PAGE2: BASEBAND & MEMORY  
PAGE3: RADIO  
PAGE4: INTERFACE  
PAGE5: POWER

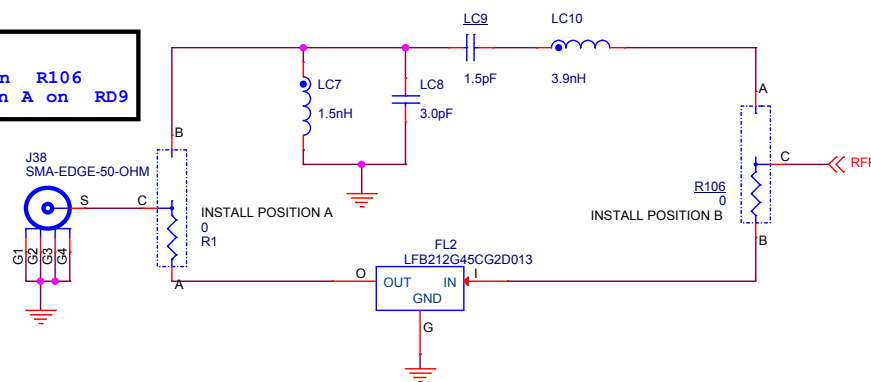
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| CYPRESS SEMICONDUCTOR ? 2016        |                                 |       |           |
| Title<br>CYW920704PEVAL2 Cover Page |                                 |       |           |
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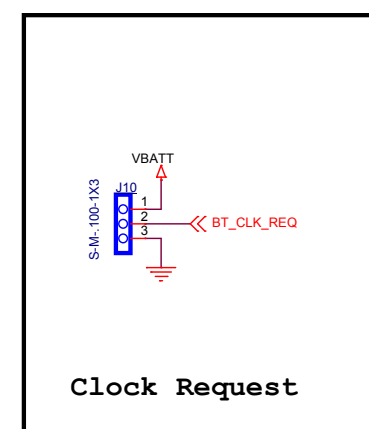
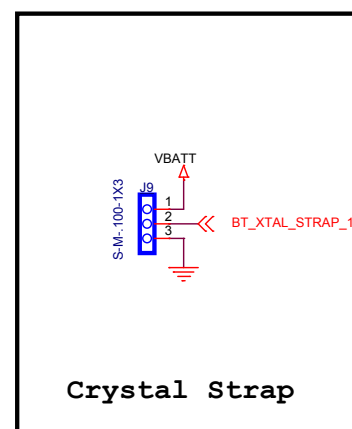
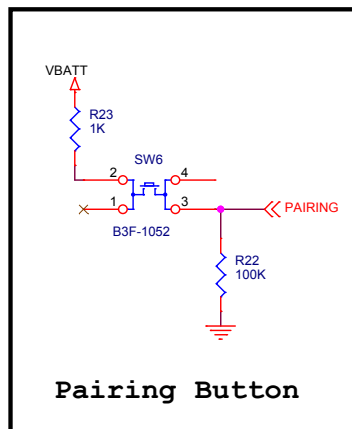
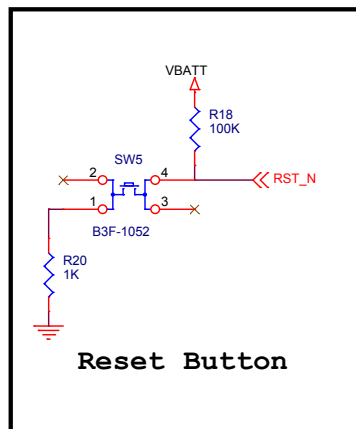
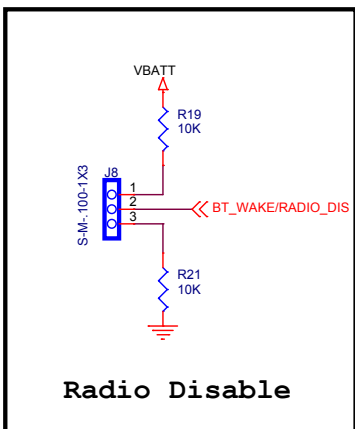
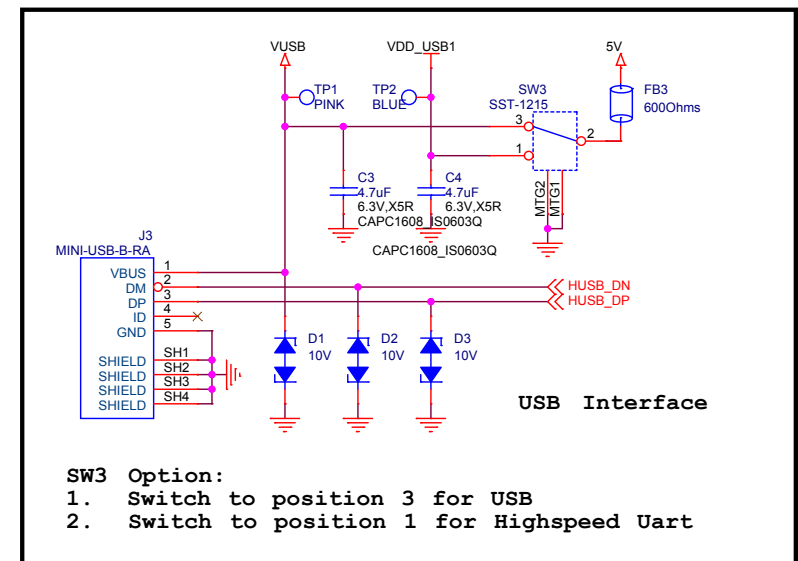
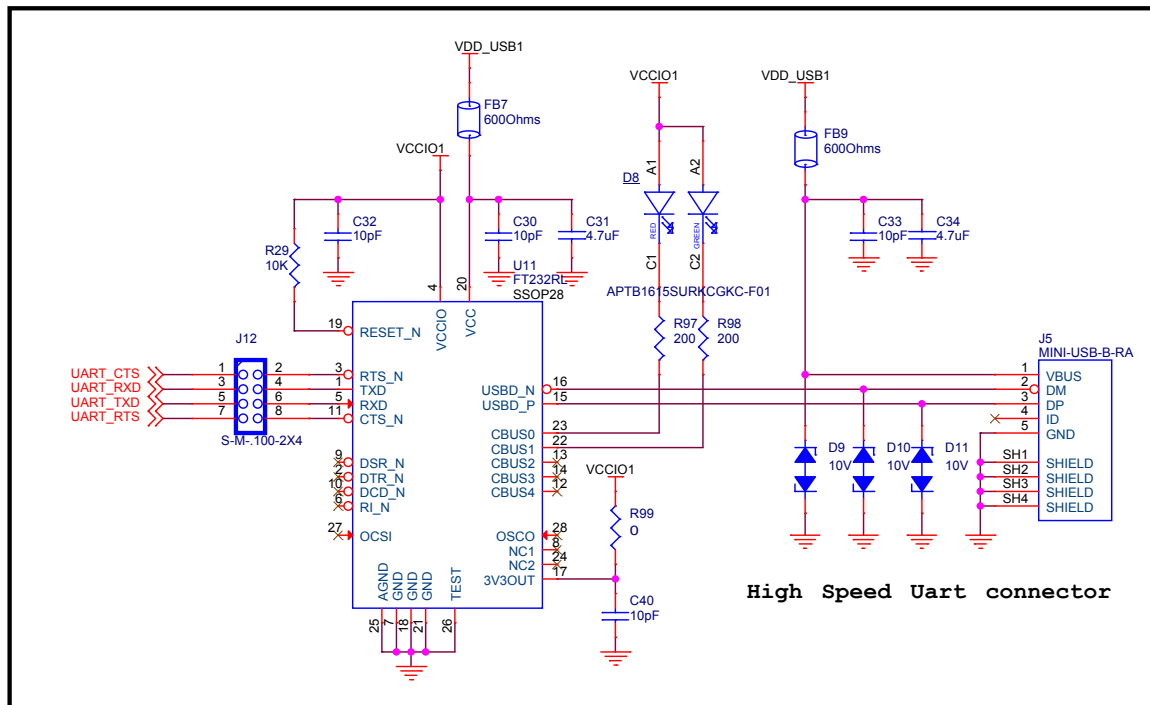


**Band Pass Filter Options:**  
 Chip BPF: Install Position A on R1 and Install Position B on R106  
 4 Element BPF: Install Position B on R1 and Install Position A on RD9



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### 3.3V 240mA LDO regulator

5V

3.3V

TP19 RED

U1  
XC6219B332MR-G  
SOT23-5

VIN VOUT  
CE VSS NC = 4

R9 10K  
C11 4.7uF  
6.3V,X5R  
CAPC1608\_IS0603Q

FB1 600Ohms  
C22 4.7uF  
6.3V,X5R  
CAPC1608\_IS0603Q

R12 220  
D4 LTST-C190GKT

J6 S-M-100-1X2

C36 0.1uF

## LED INDICATORS

The image displays two circuit diagrams for LED indicators. Both diagrams show a blue LED connected in series with a resistor and then connected to ground.

**Left Diagram:**

- Power source: VUSB
- LED: D5, APT1608SGD3
- Resistor: R46, 470, 5%

**Right Diagram:**

- Power source: VDD\_USB1
- LED: D6, APT1608SGD3
- Resistor: R47, 470, 5%

### 3.3V Jumper

The diagram illustrates the 3.3V Jumper setup. A 0.1uF capacitor (C29) is connected between the VBATT pin and ground. A 3P3V pin is also shown. A 3.3V jumper (J1) is connected between the 3P3V pin and the 3.3V pin of the S-M-100-1X2 module.

## TEST HEADERS

**J14**

|   |      |   |          |
|---|------|---|----------|
| 1 | R64  | 0 | UART_CTS |
| 2 | R65  | 0 | UART_RTS |
| 3 | R67  | 0 | UART_TXD |
| 4 | R68  | 0 | UART_RXD |
| 5 | R104 | 0 | SI/SDA   |
| 6 | R102 | 0 | SO/SCL   |
| 7 | R105 | 0 | CLK_C    |
| 8 | R103 | 0 | CSN_C    |

S-M-100-1X8

**J16**

|   |      |   |                                                    |
|---|------|---|----------------------------------------------------|
| 1 | R109 | 0 | GPIO2<br>GPIO3<br>GPIO4<br>GPIO5<br>GPIO6<br>GPIO7 |
| 2 | R94  | 0 |                                                    |
| 3 | R72  | 0 |                                                    |
| 4 | R91  | 0 |                                                    |
| 5 | R81  | 0 |                                                    |
| 6 | R74  | 0 |                                                    |
| 7 | R73  | 0 |                                                    |
| 8 | R89  | 0 |                                                    |

S-M-100-1X8

Ground

**J17**

|   |     |   |                   |
|---|-----|---|-------------------|
| 1 | R75 | 0 | RST_N             |
| 2 | R76 | 0 | JTAG_SEL          |
| 3 | R77 | 0 | BT_WAKE/RADIO_DIS |
| 4 | R79 | 0 | BT_HOST_WAKE      |
| 5 | R80 | 0 | PCM_IN            |
| 6 | R82 | 0 | PCM_OUT           |
| 7 | R83 | 0 | PCM_CLK           |
| 8 | R93 | 0 | PCM_SYNC          |

S-M-100-1X8

## EXTERNAL TCXO

EX\_CLK/TCXO

R92  
0 (DNI)

CL331-0471-0-01 (DNI)

J19

C44  
10pF

S

G2

G1

