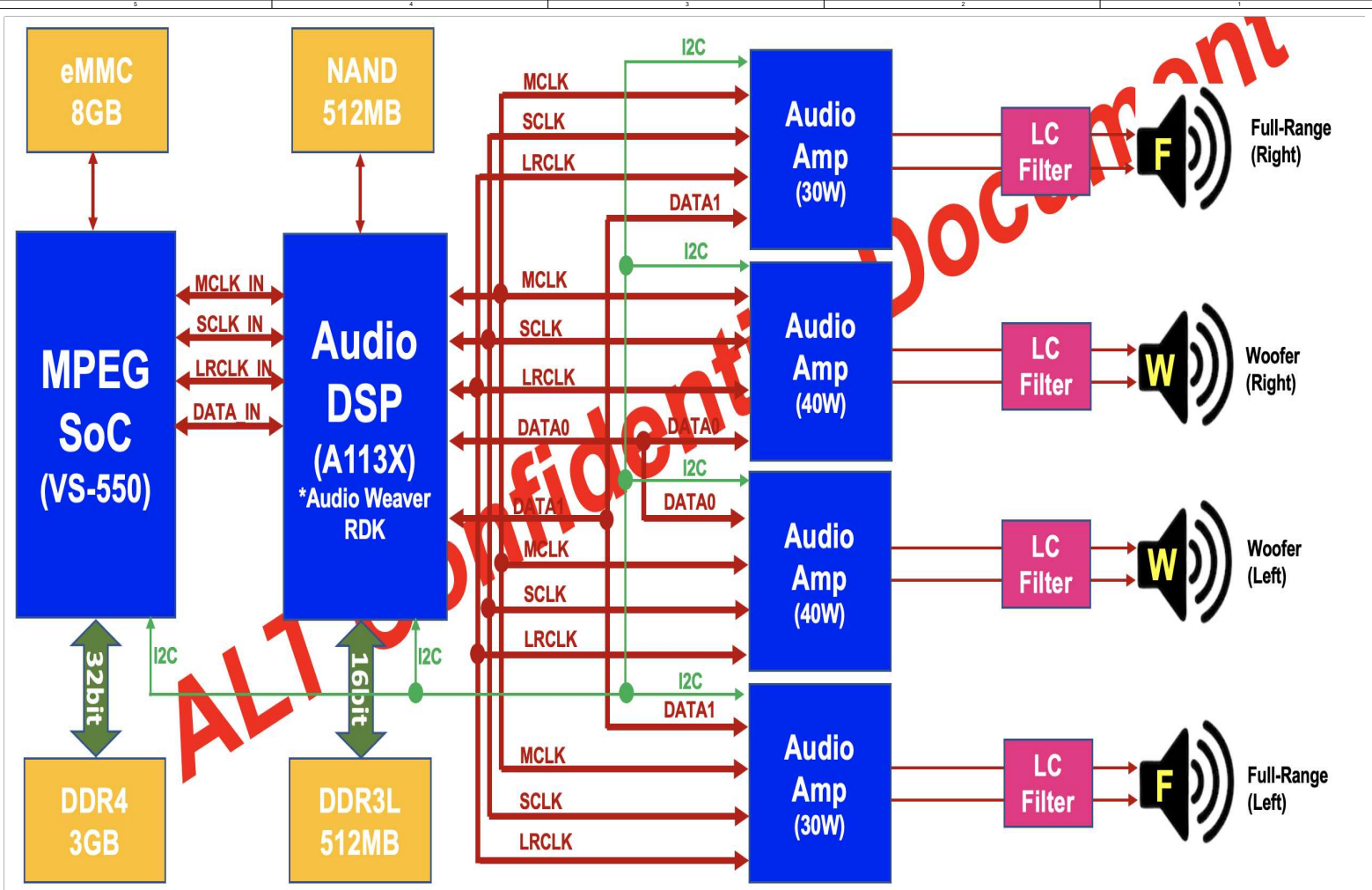
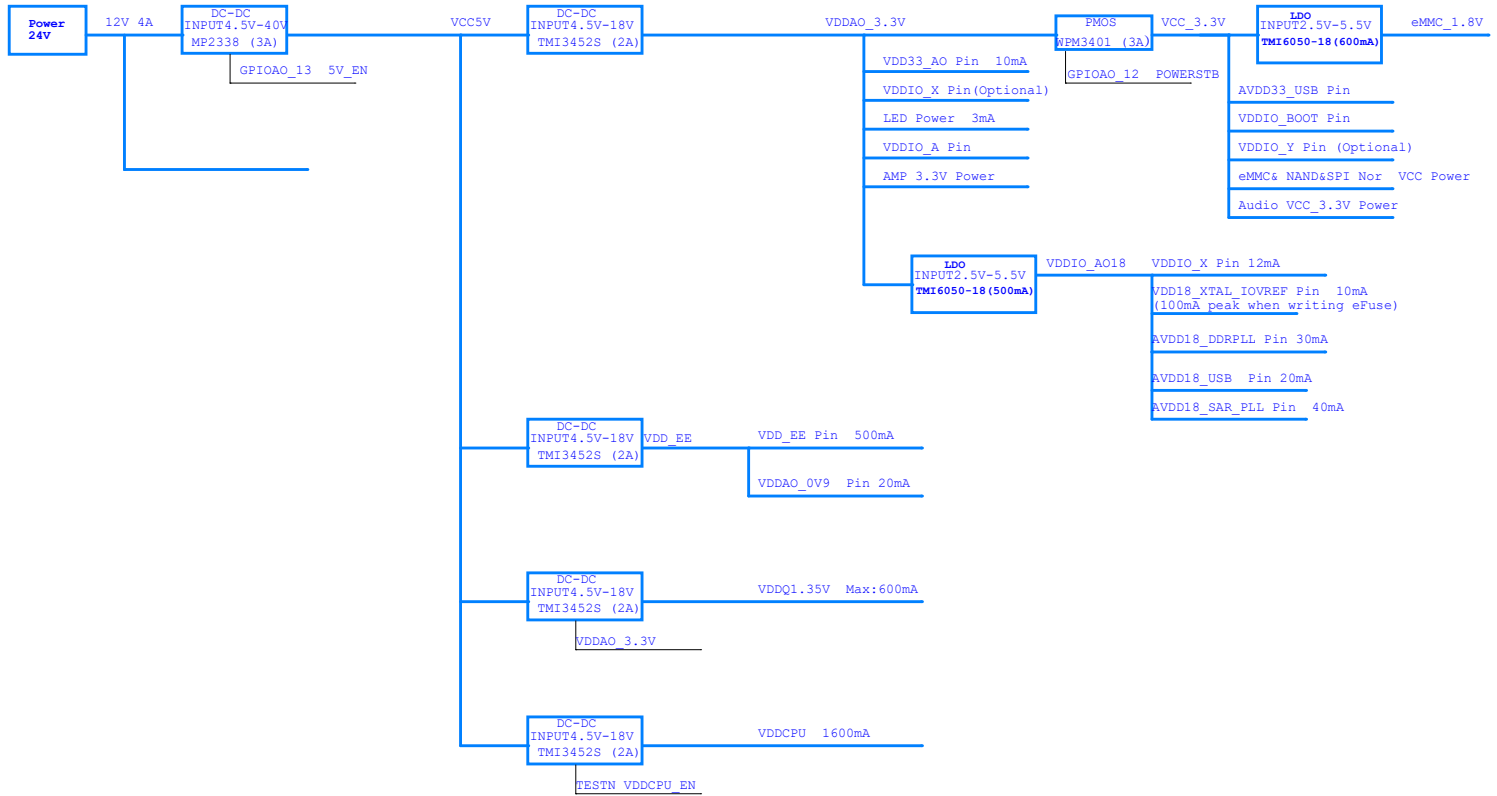
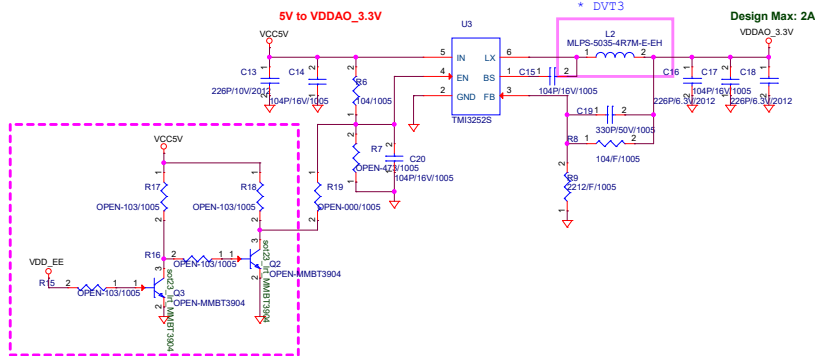
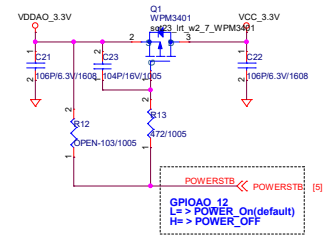
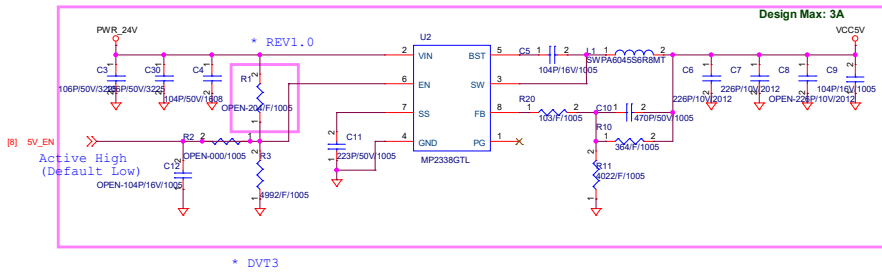
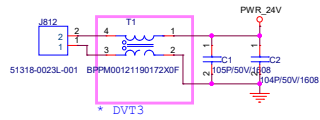




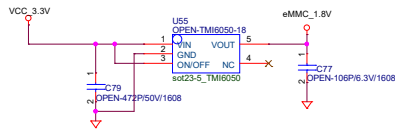
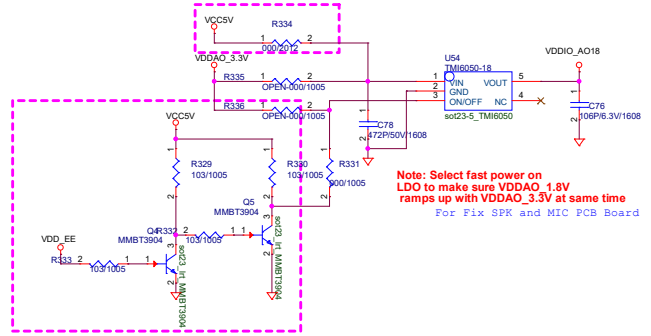
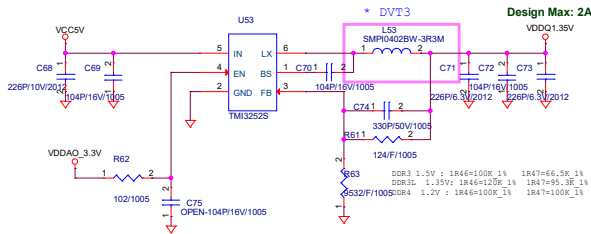
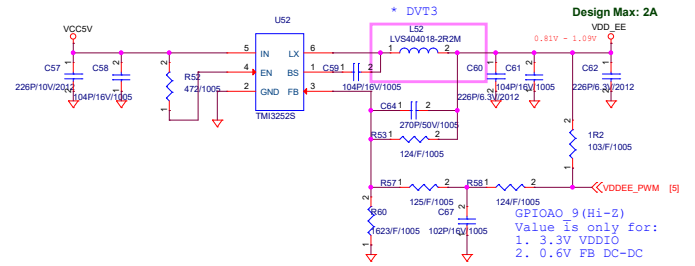
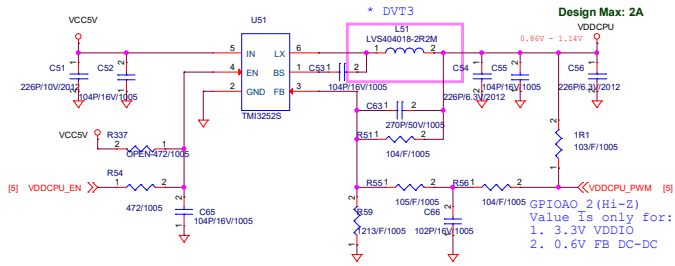
Standby power supply



ALT		
02. POWER TREE		
File	Document Number	Rev
A3	A12 MAX-AMP B/D	8.31
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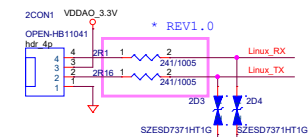


ALT			
03. POWER PART 1			
Size	Document Number	Rev	
A3	A12 MAX-AMP BID	8.31	
Date	Monday, August 02, 2021	Sheet	3 of 11

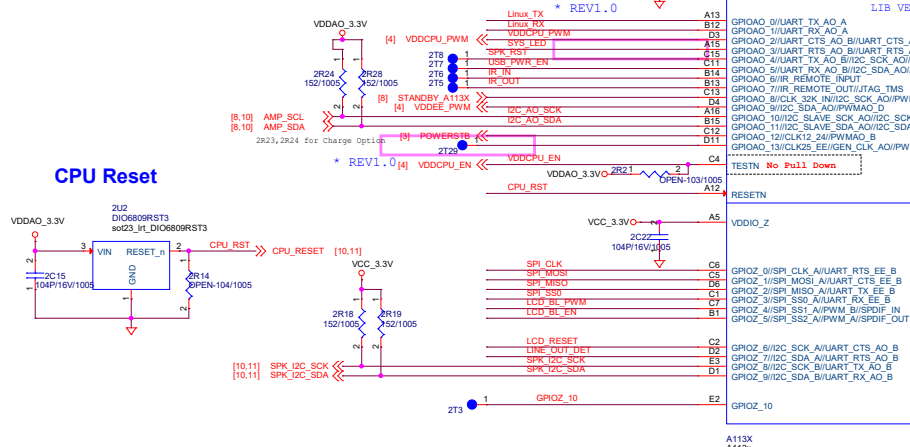


ALT		
04. POWER PART2		
Size A3	Document Number A12 MAX-AMP B10	Rev 8.1
Date Monday, August 02, 2021	Sheet 4 of 11	

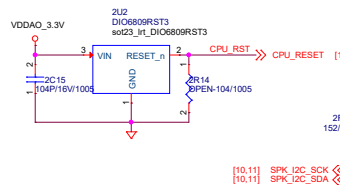
For Linux Debug



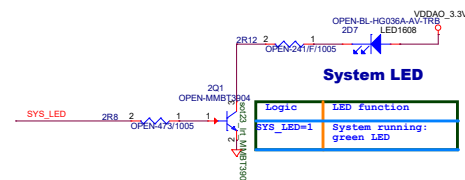
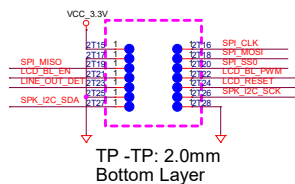
mark signal name on PCB



CPU Reset



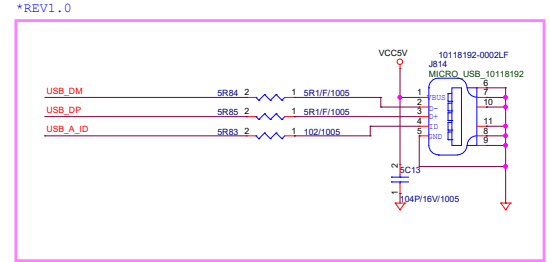
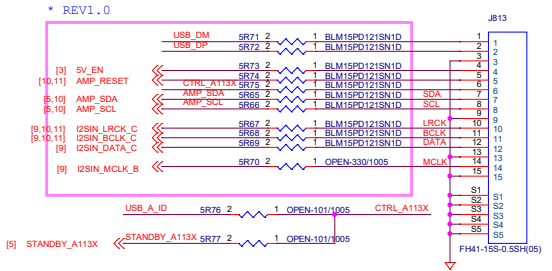
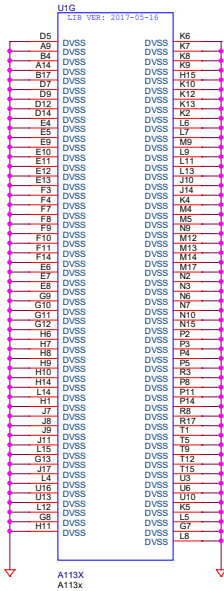
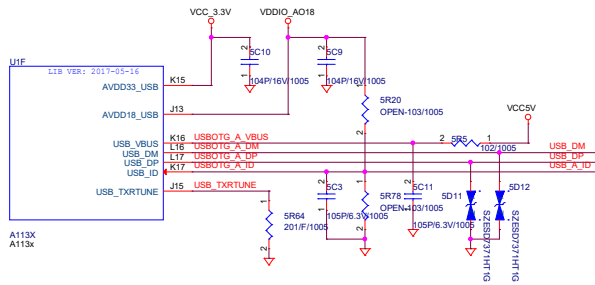
SPI & I2C IO Debug



HW ID	0.1	0.2	0.3	1.0	1.1
ADC Data	248	334	420	512	603
PULL-UP	75K	58K	56K	47K	39K
PULL-DOWN	24K	33K	39K	47K	56K
VERSION	0				

Title			
05. A113X_IO			
Size A3	Document Number	Rev	
	A12 MAX-AMP B/D	0.31	
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ALT			
08. A113X_POWER			
Size	Document Number	Rev	
A3	A12 MAX-AMP B/D	8.31	
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TP-TP: 2.5mm
Bottom Layer

LEFT WOOFER

Device Address: 0x54

LEFT FULL RANGE

Device Address: 0x56

10. AUDIO AMP_LEFT			
Size A3	Document Number A12 MAX-AMP BID	Rev 8.31	
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