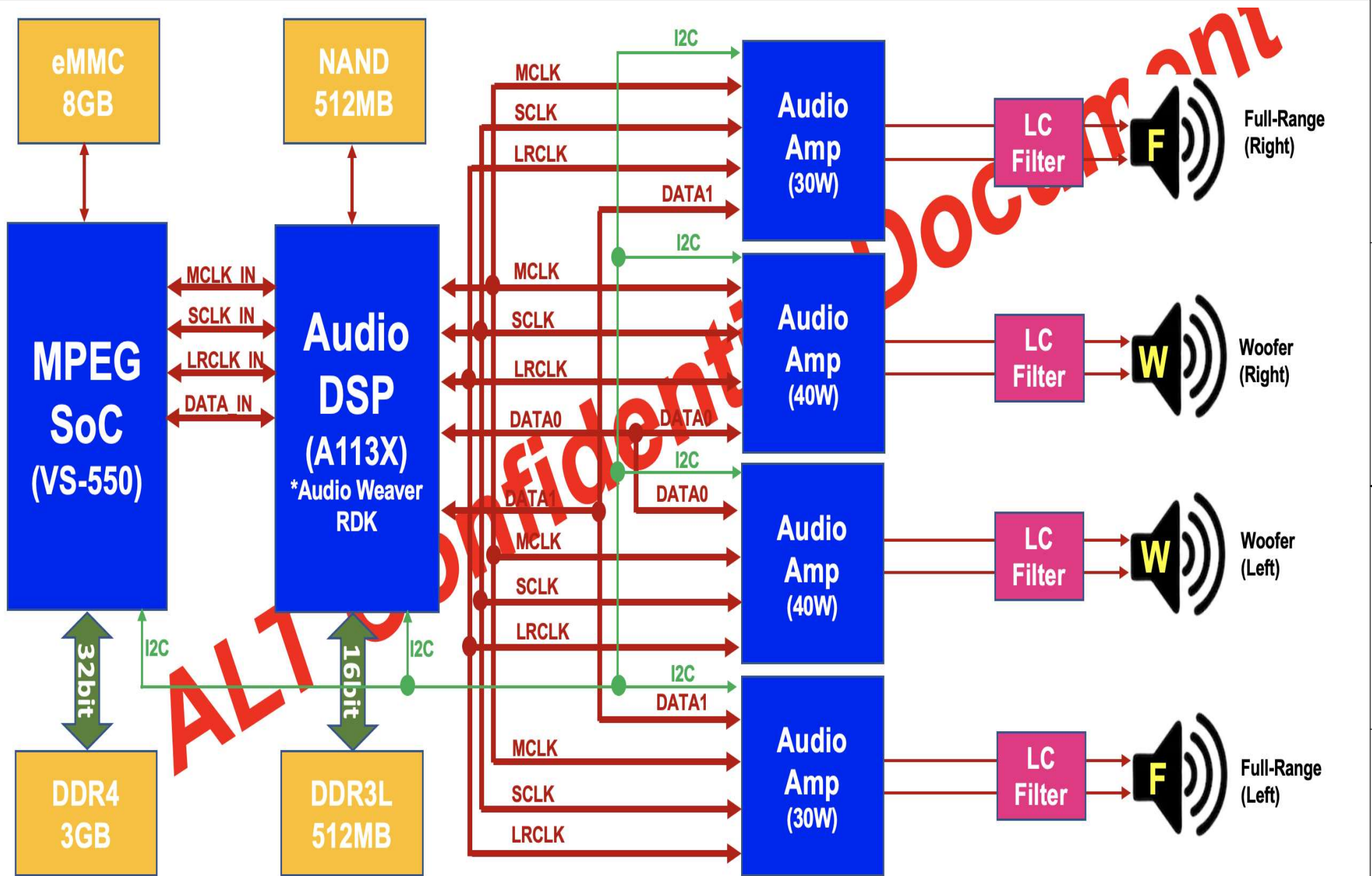
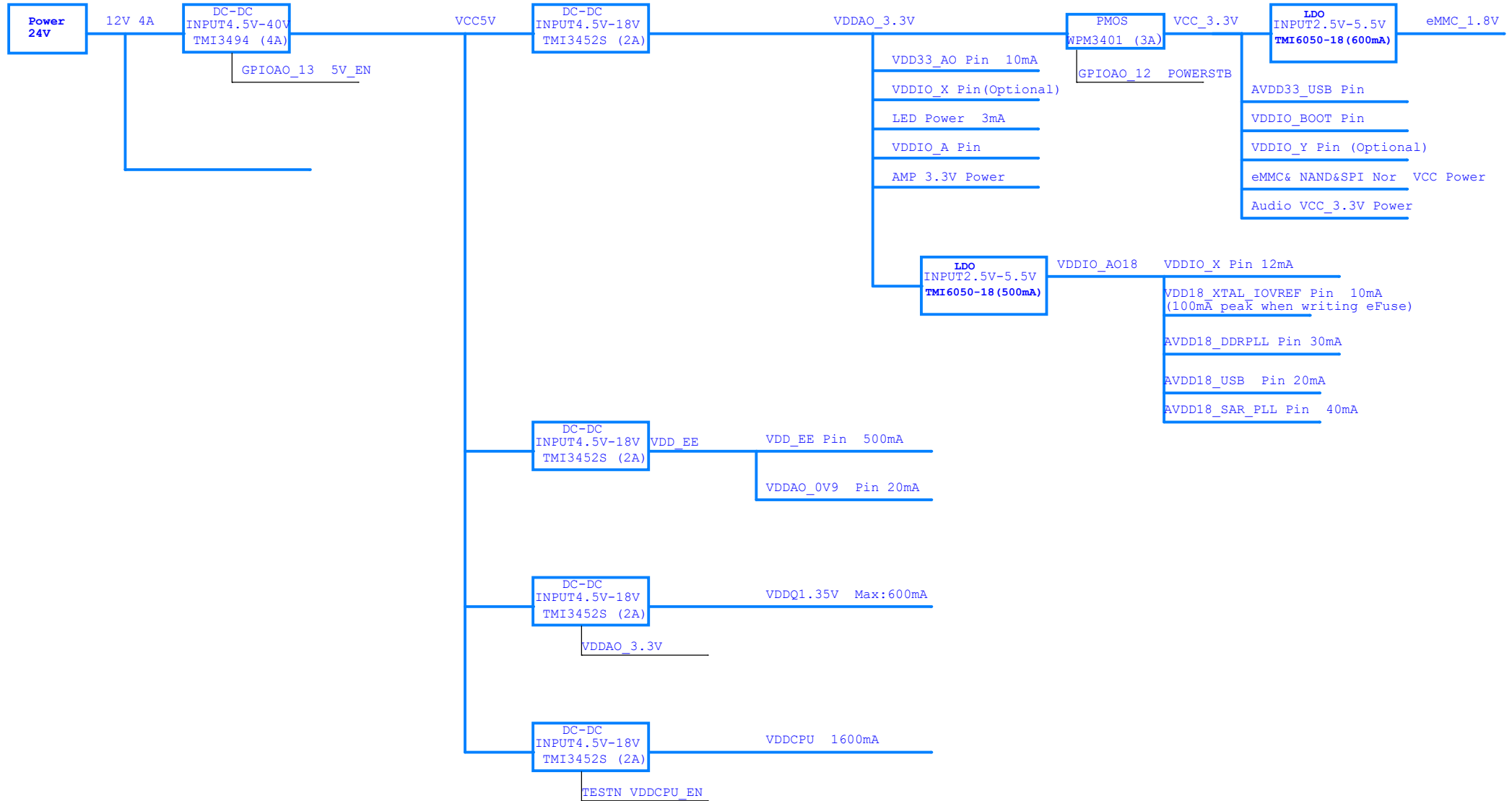
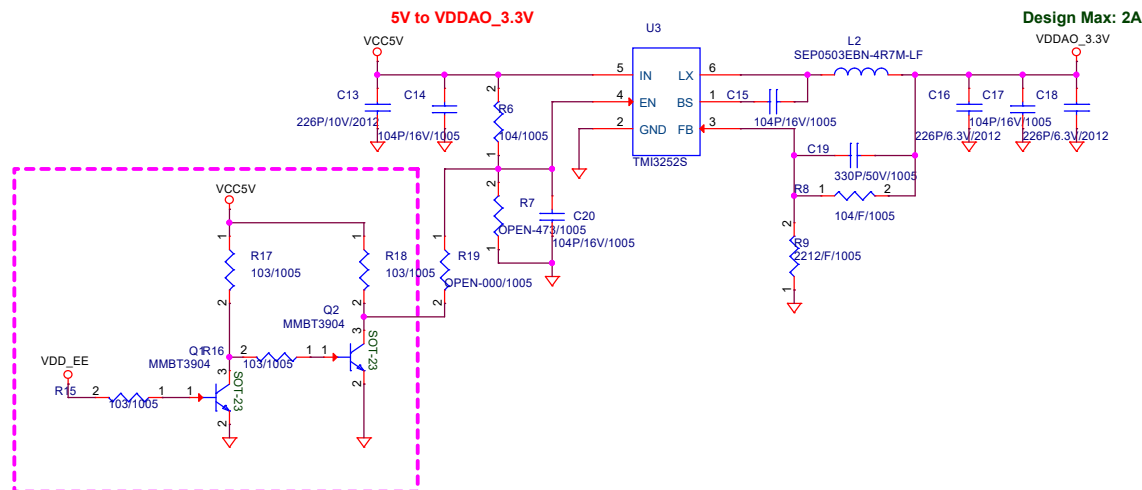
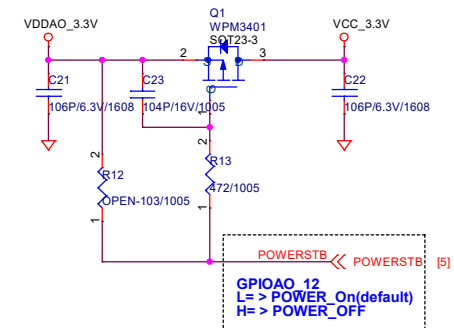
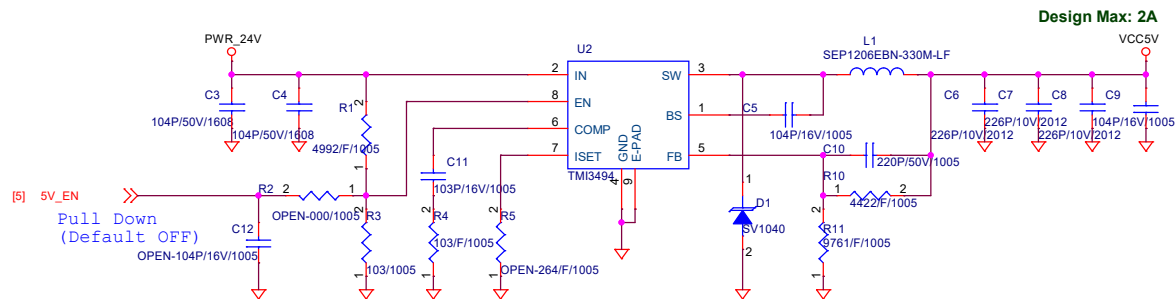
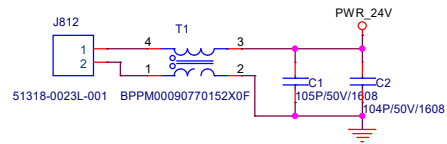
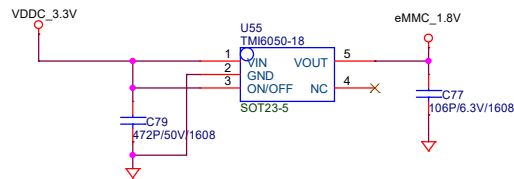
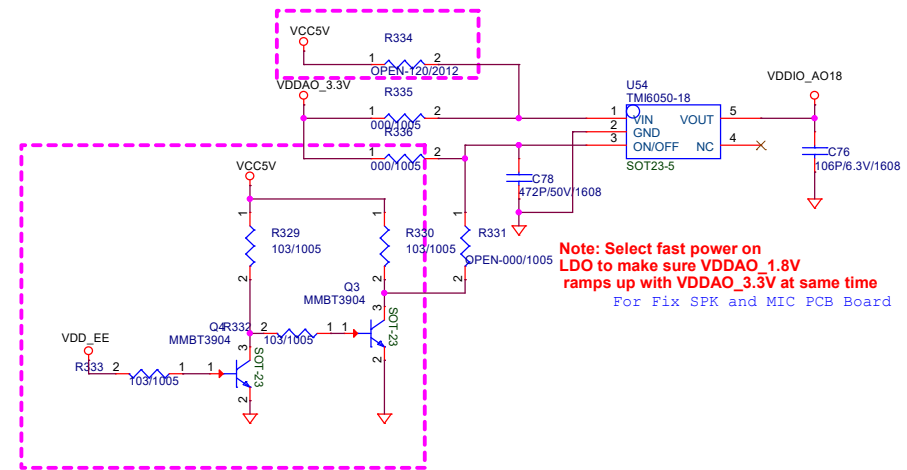
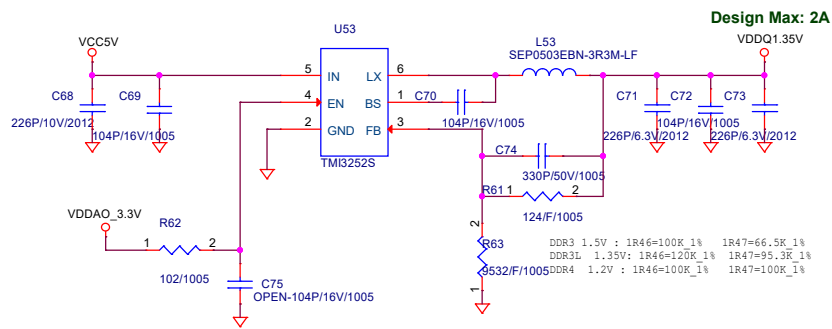
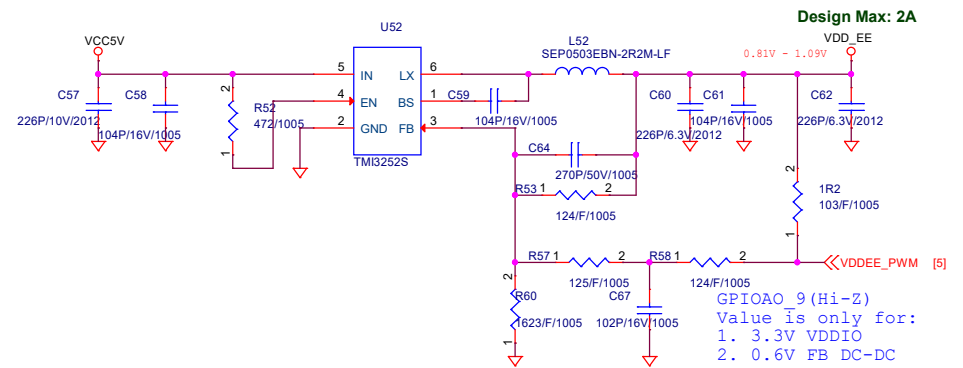
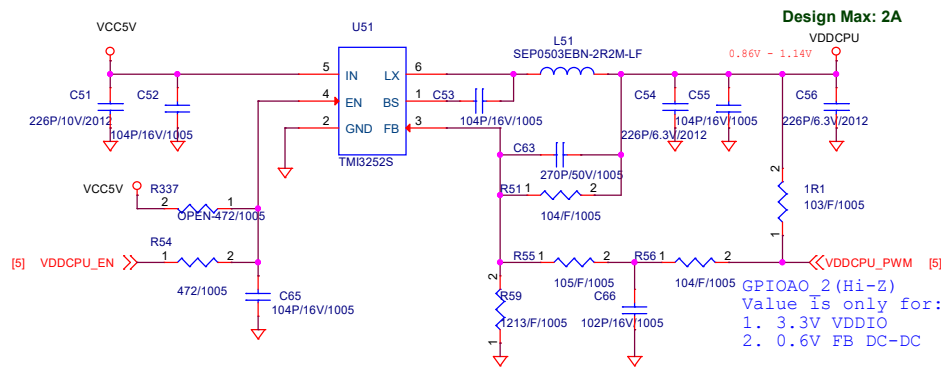




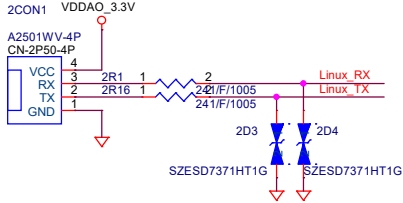
Standby power supply



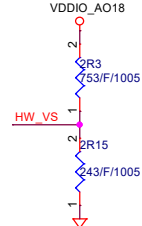
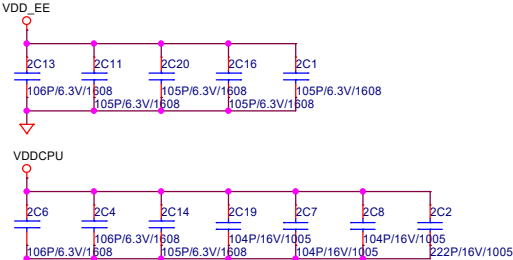




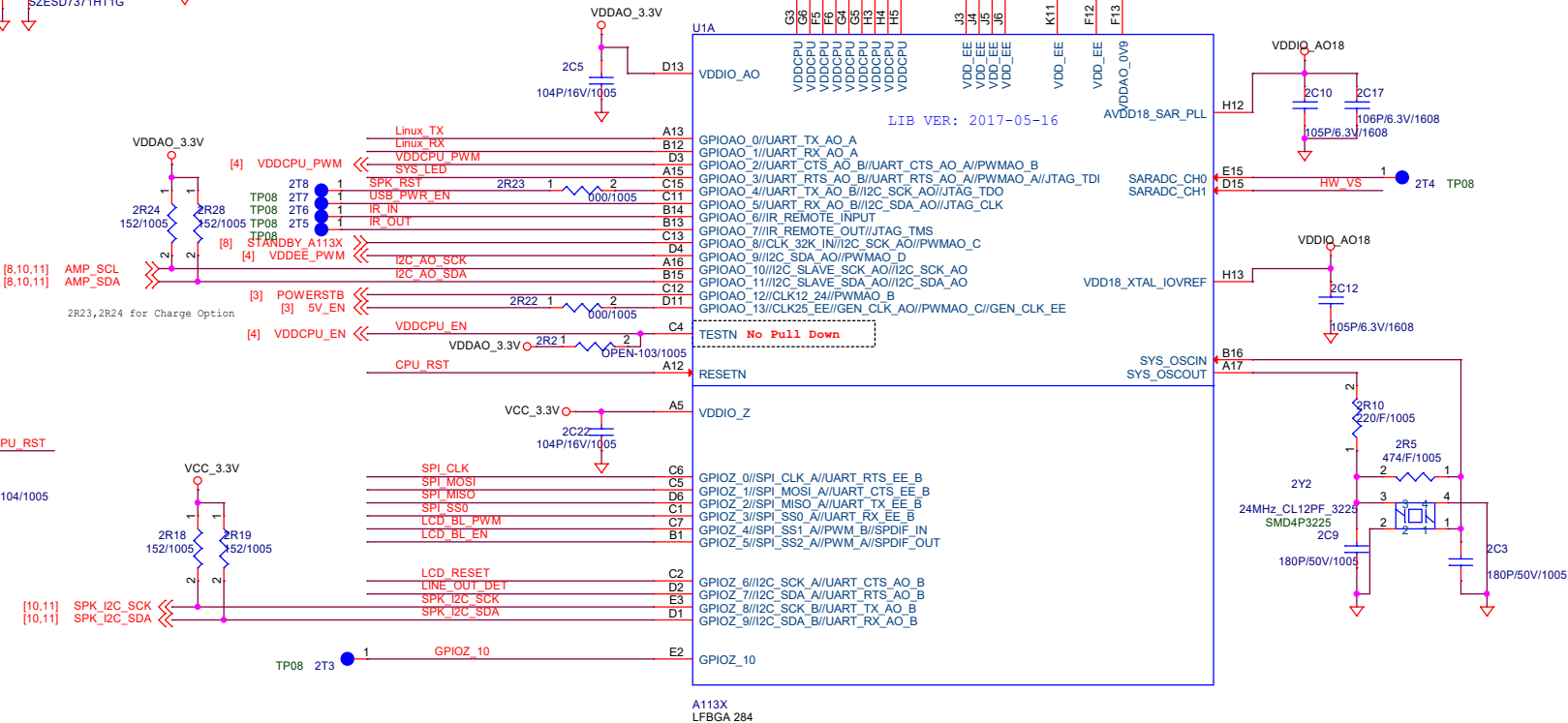
For Linux Debug



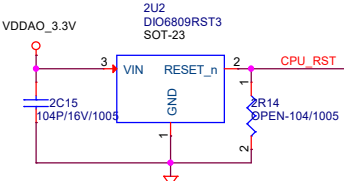
mark signal name on PCB



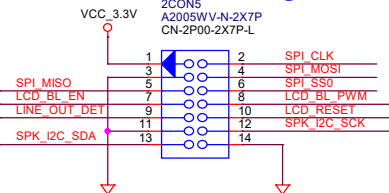
HW ID	0.1	0.2	0.3	1.0	1.1
ADC Data	248	334	420	512	603
PULL-UP	75K	58K	56K	47K	39K
PULL-DOWN	24K	33K	39K	47K	56K
VERSION	0				



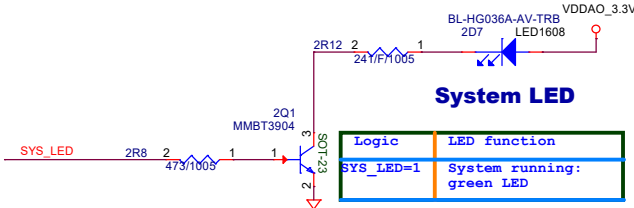
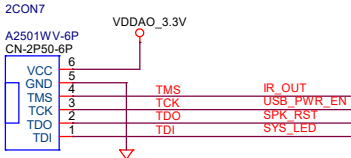
CPU Reset



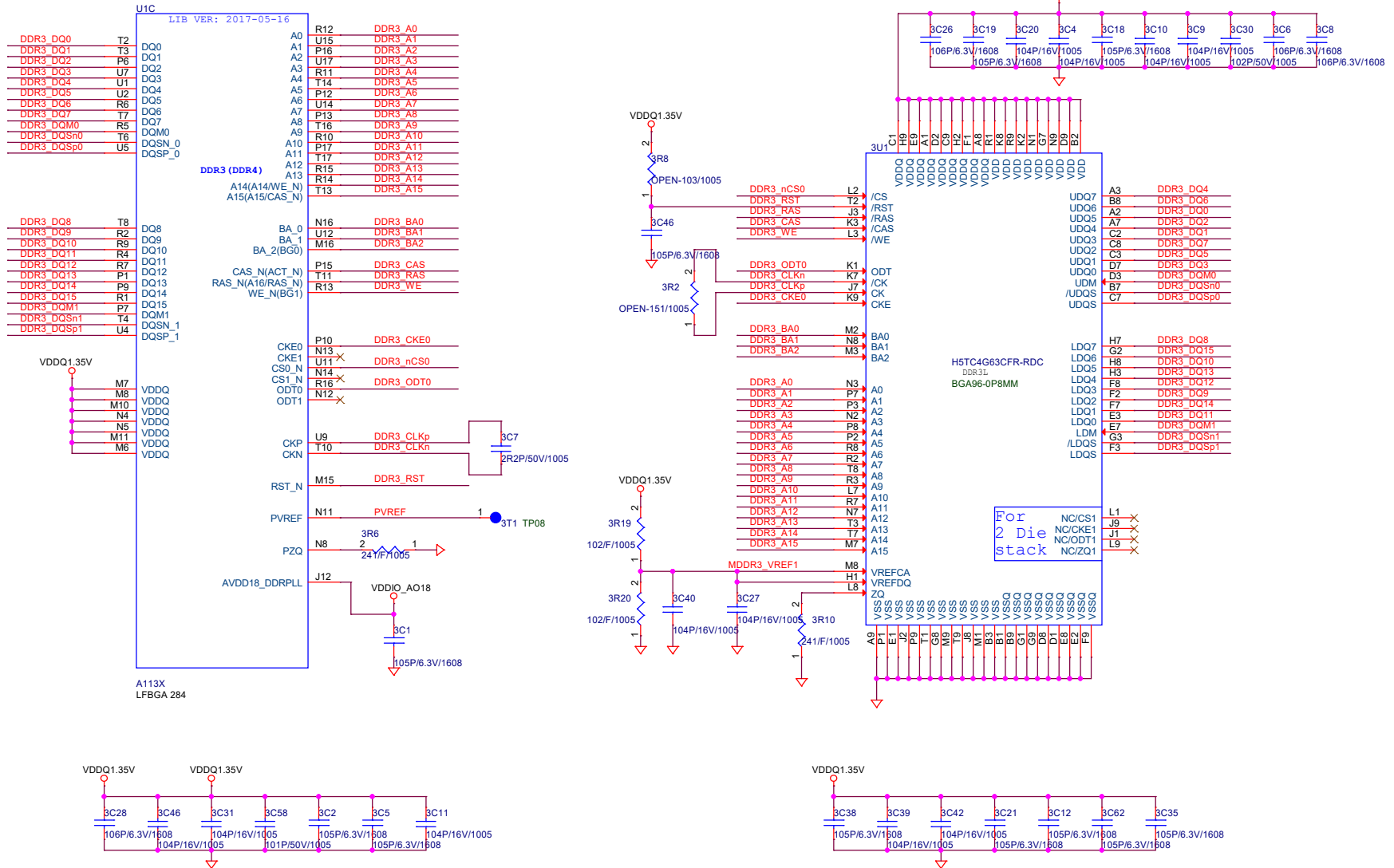
SPI & I2C IO Debug

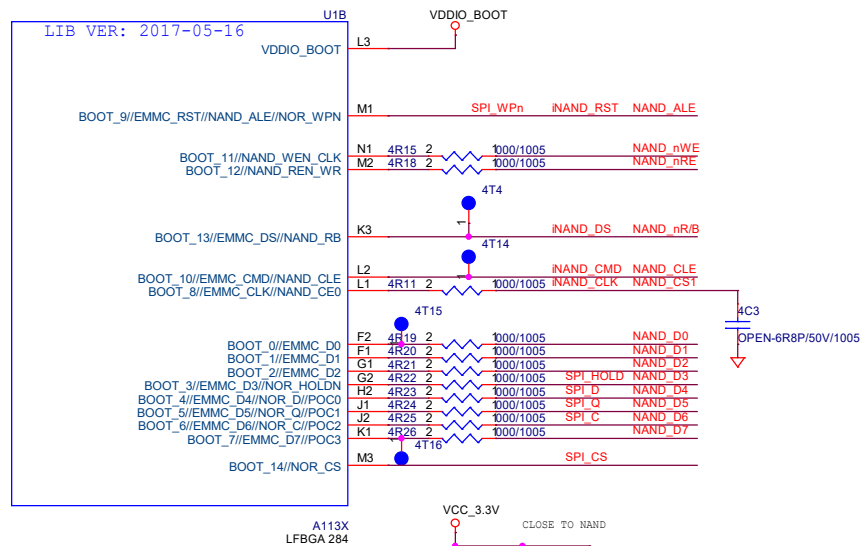
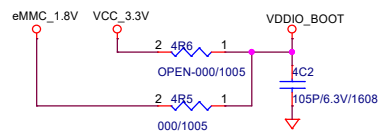


ARM JTAG Debug



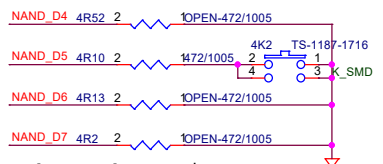
mark signal name on PCB





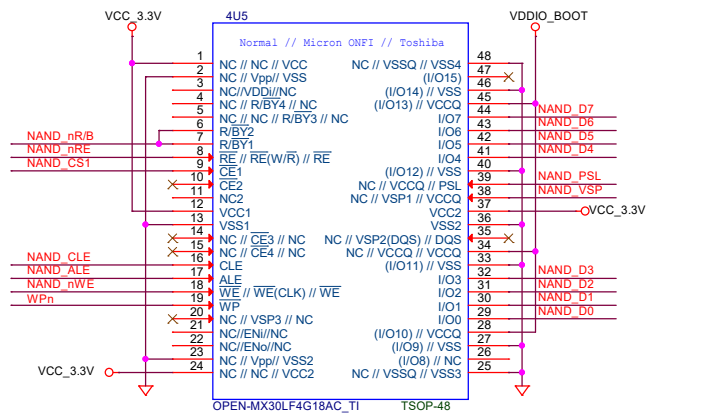
Power ON Config

```
BOOT_5[NAND_D5]: 0= USB Boot First
BOOT_6[NAND_D6]: 0= SPI NOR Boot First
```



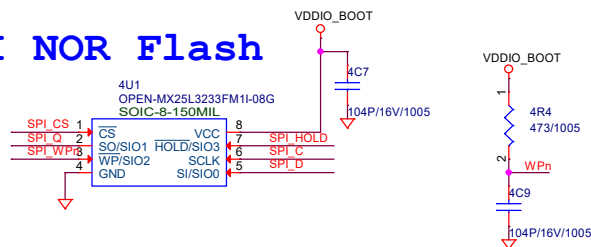
close to branch point

NAND (TSOP)



Only Support SLC NAND Flash
Not Support MLC NAND Flash

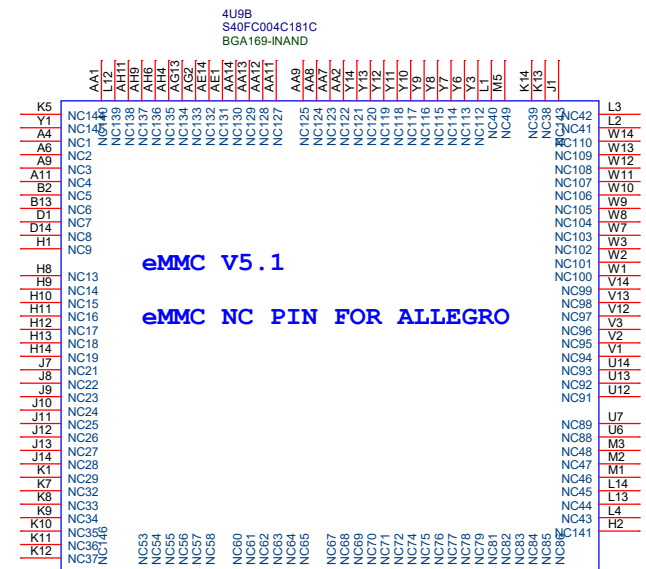
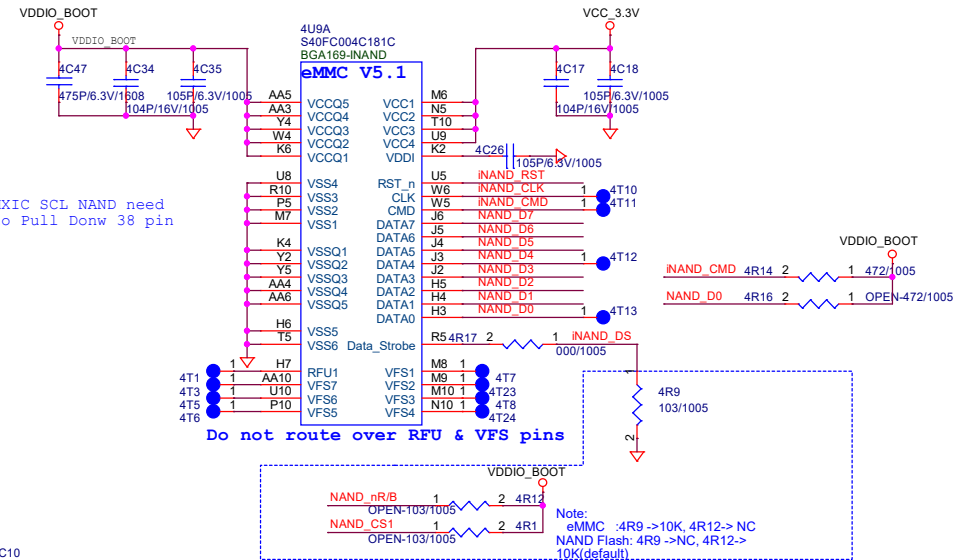
SPI NOR Flash



Use 1.8V power SPI nor Flash if VDDIO BOOT is 1.8V.

eMMC/iNAND
PCB Decal:169pin balls Type(BGA)

VCC = 2.7 - 3.6V



Title		 ALT	
07. A113X_FLASH			
Size A3	Document Number	Rev	
	A12 MAX-AMP B/D	0.1	
Date:	Tuesday, February 23, 2021	Sheet	7 of 11

A113X
LFBGA 284

A113X
LFBGA 284

