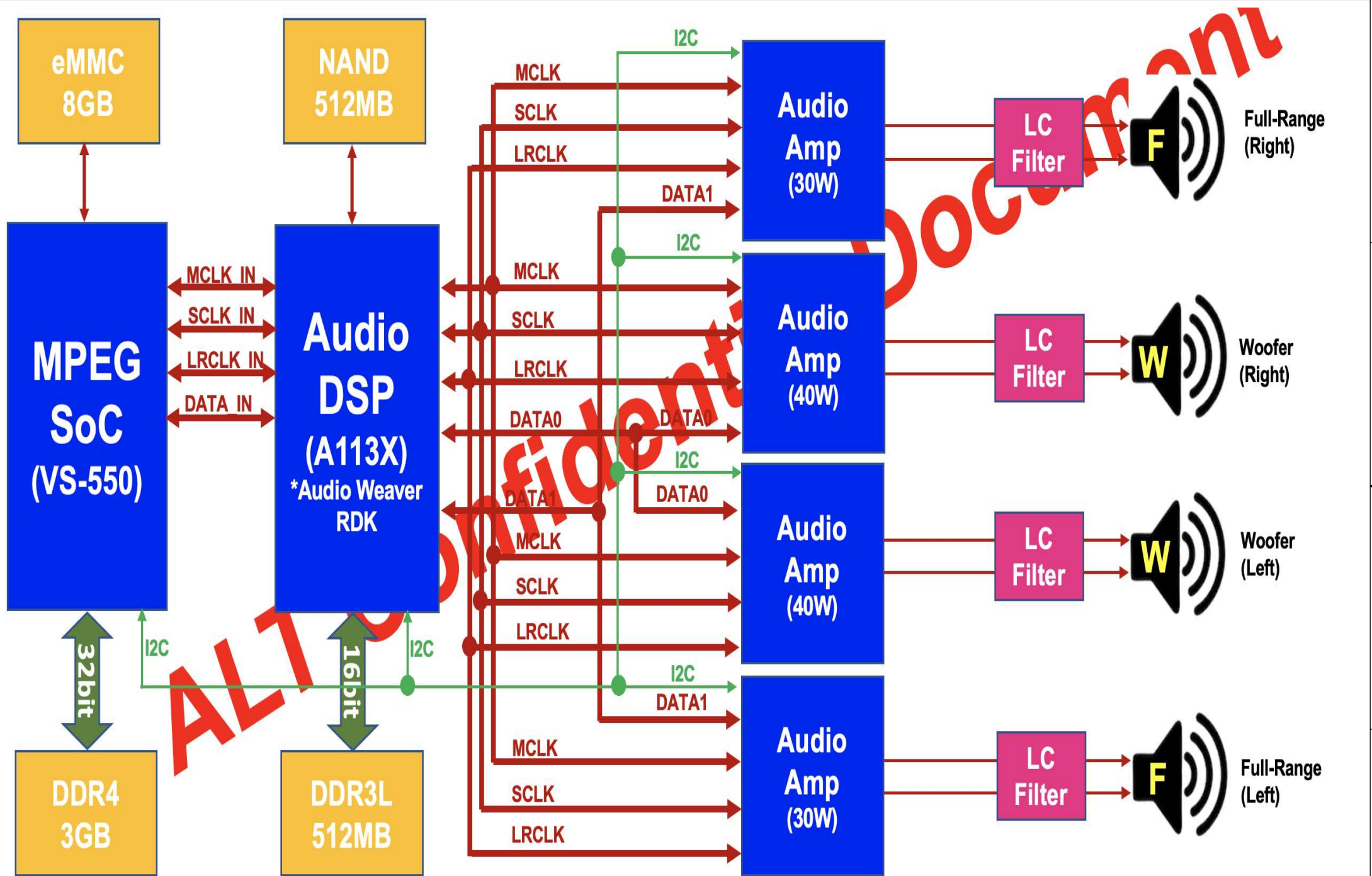




Standby power supply

Power 24V

12V 4A

DC-DC INPUT4.5V-40V MP2338 (3A)

GPIOAO_13 5V_EN

VCC5V

DC-DC INPUT4.5V-18V TMI3452S (2A)

VDDAO_3.3V

VDD33_AO Pin 10mA

VDDIO_X Pin(Optional)

LED Power 3mA

VDDIO_A Pin

AMP 3.3V Power

PMOS WPM3401 (3A)

GPIOAO_12 POWERSTB

VCC_3.3V

LDO INPUT2.5V-5.5V TMI6050-18 (600mA)

eMMC_1.8V

AVDD33_USB Pin

VDDIO_BOOT Pin

VDDIO_Y Pin (Optional)

eMMC& NAND&SPI Nor VCC Power

Audio VCC_3.3V Power

VDDIO_AO18

VDDIO_X Pin 12mA

VDD18_XTAL IOVREF Pin 10mA (100mA peak when writing eFuse)

AVDD18_DDRPLL Pin 30mA

AVDD18_USB Pin 20mA

AVDD18_SAR_PLL Pin 40mA

DC-DC INPUT4.5V-18V TMI3452S (2A)

VDD_EE

VDD_EE Pin 500mA

VDDAO_0V9 Pin 20mA

DC-DC INPUT4.5V-18V TMI3452S (2A)

VDDQ1.35V Max:600mA

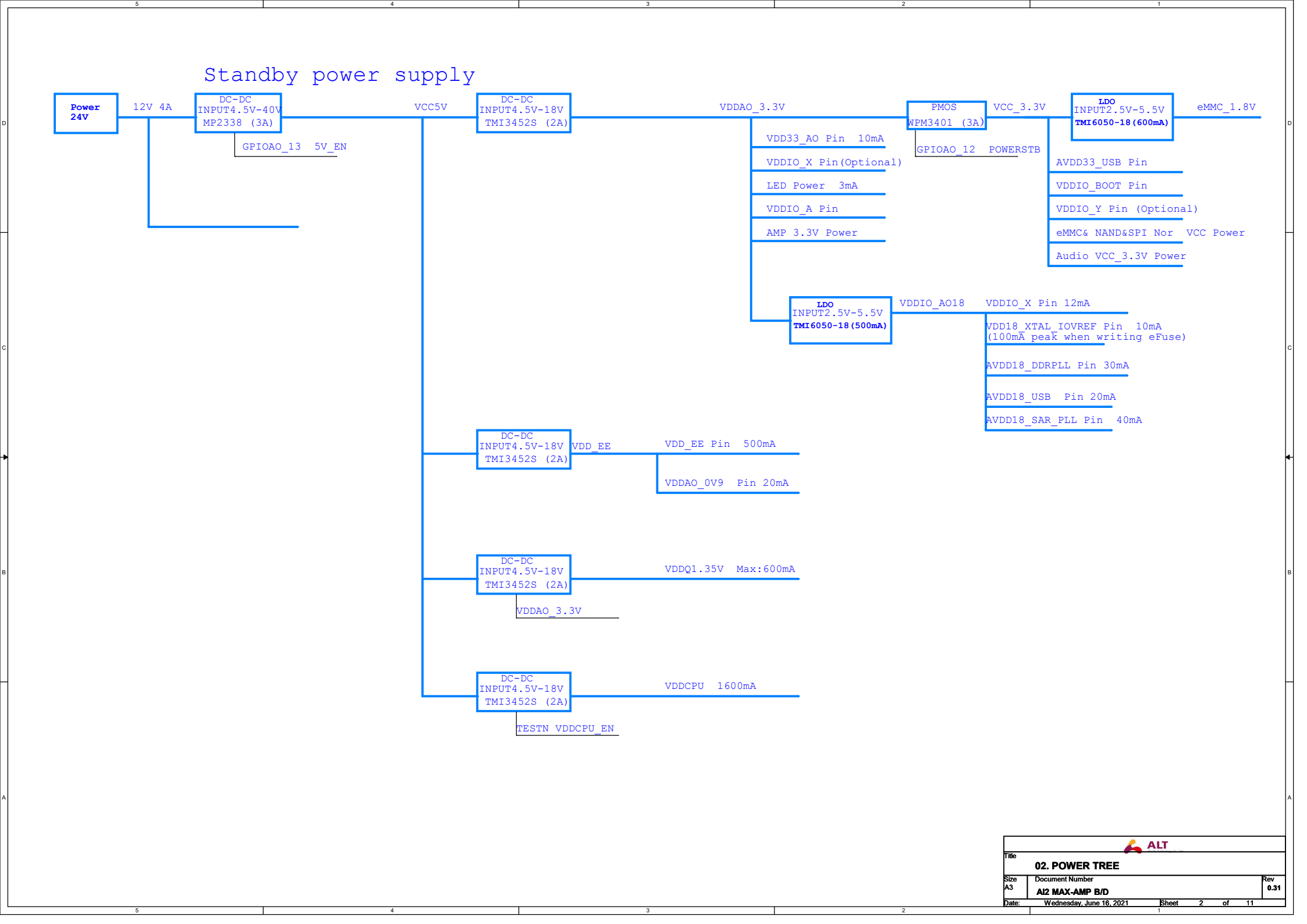
VDDAO_3.3V

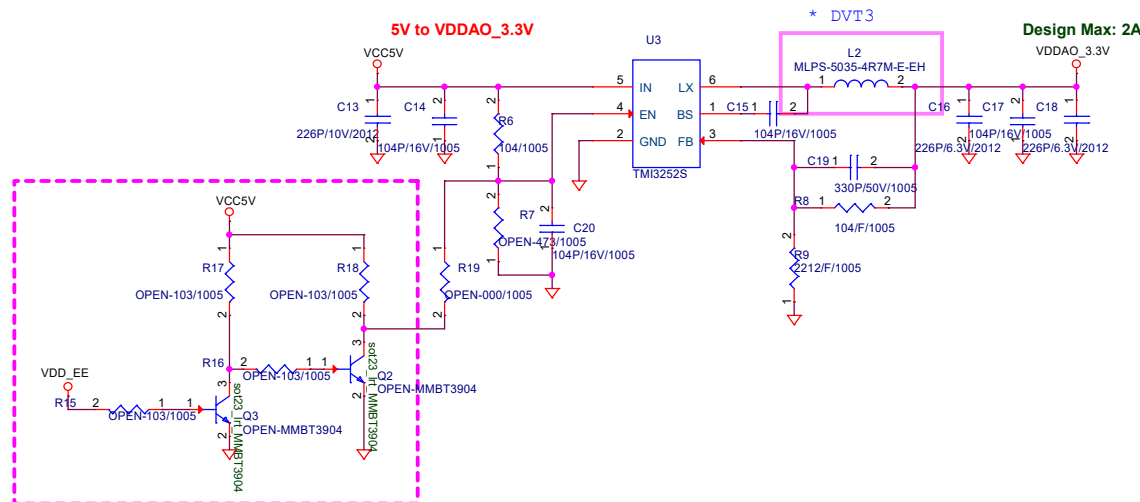
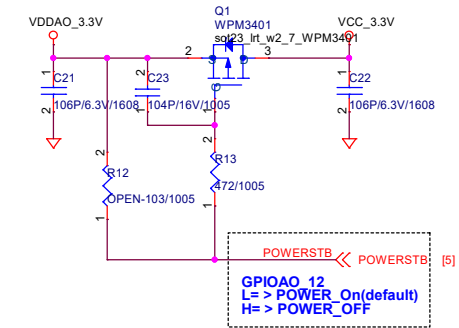
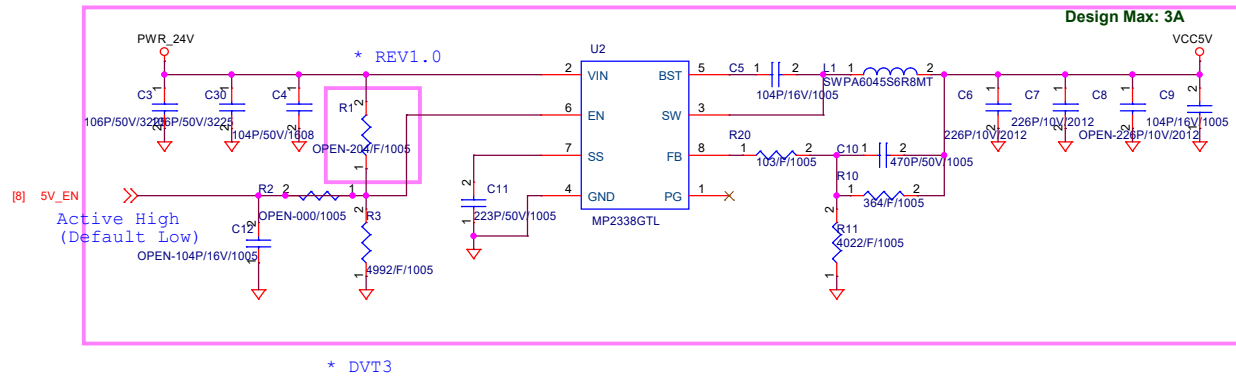
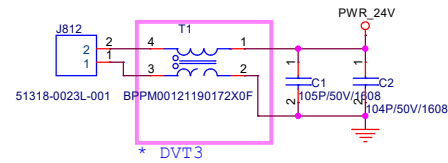
DC-DC INPUT4.5V-18V TMI3452S (2A)

VDDCPU 1600mA

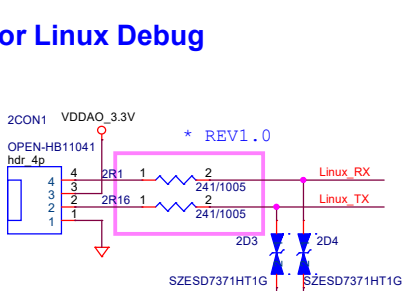
TESTN VDDCPU_EN

Title		02. POWER TREE	
Size A3	Document Number	Rev 0.31	
Date:	Wednesday, June 16, 2021	Sheet 2	of 11

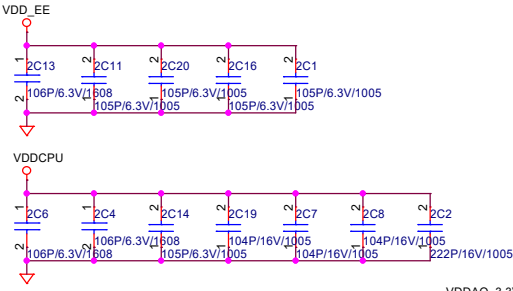




For Linux Debug

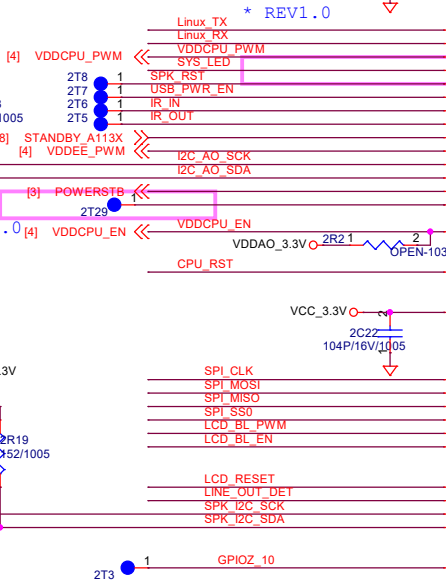
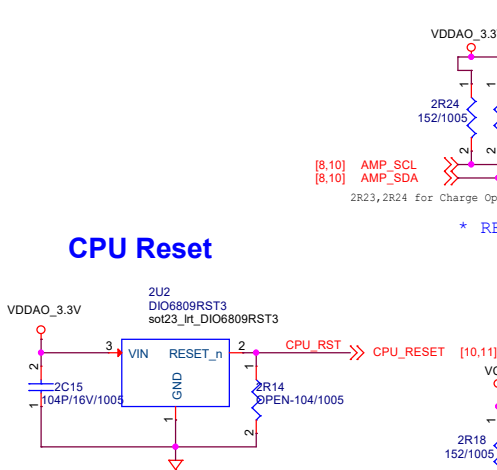


mark signal name on PCB

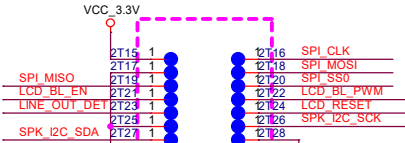


HW ID	0.1	0.2	0.3	1.0	1.1
ADC Data	248	334	420	512	603
PULL-UP	75K	58K	56K	47K	39K
PULL-DOWN	24K	33K	39K	47K	56K
VERSION	O				

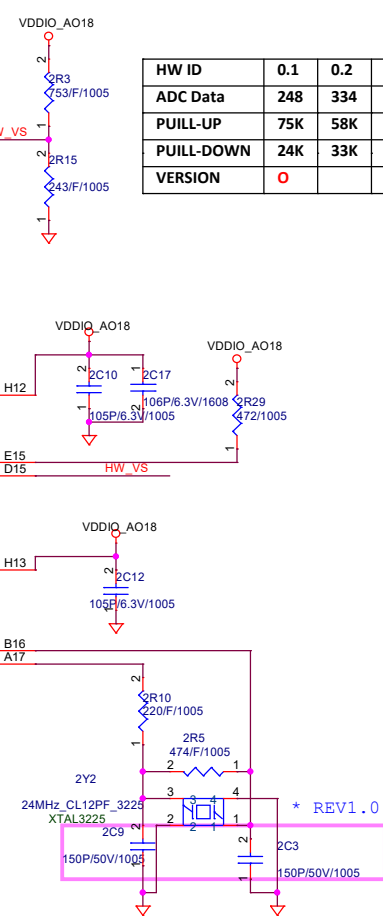
CPU Reset



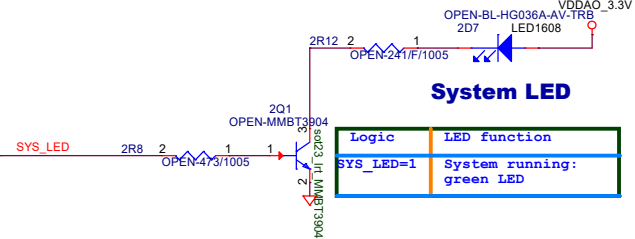
SPI & I2C IO Debug



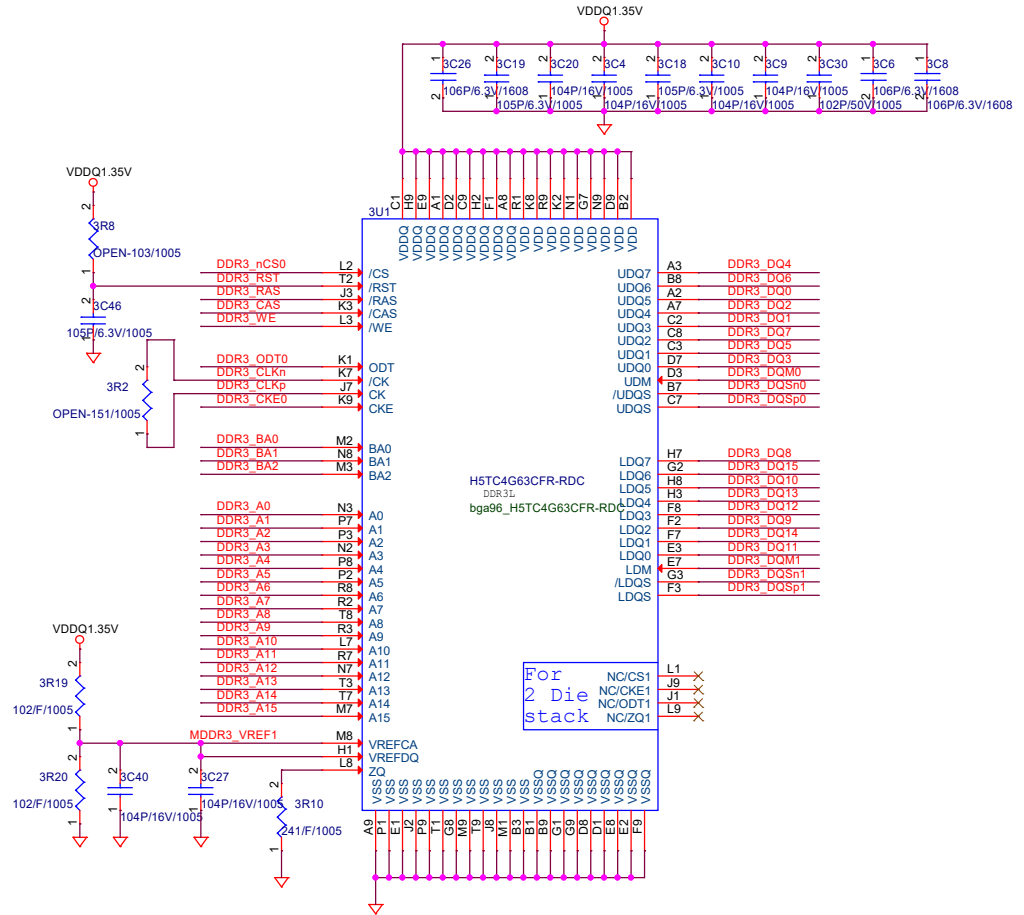
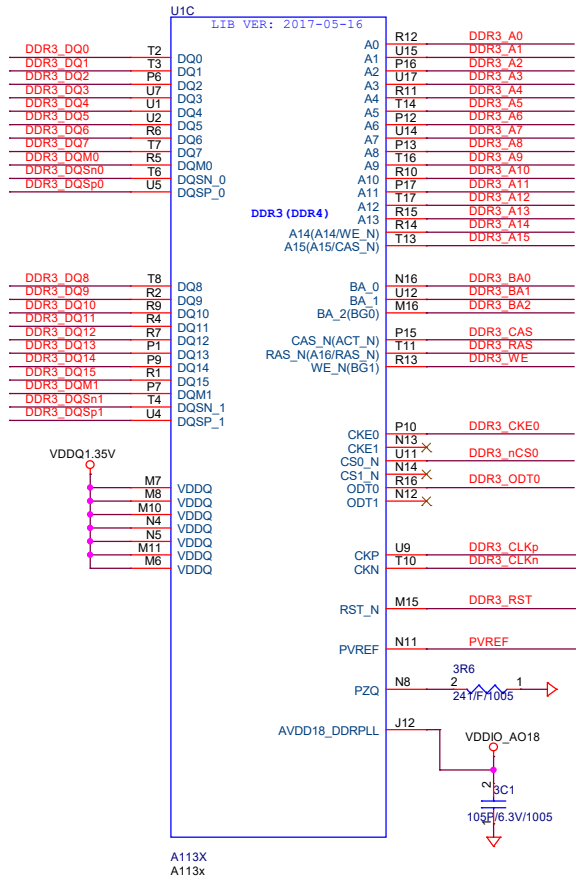
TP -TP: 2.0mm
Bottom Layer

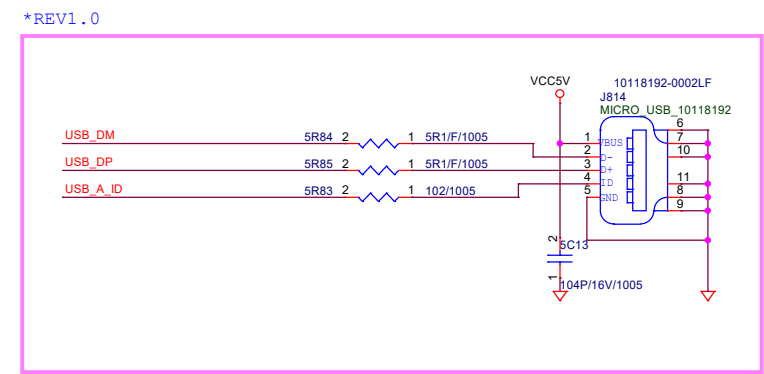
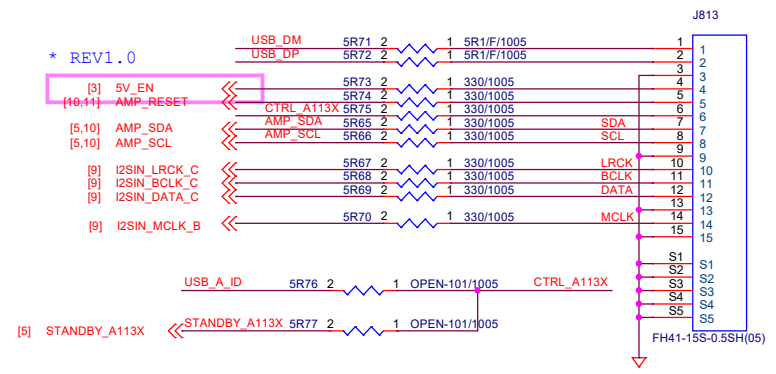
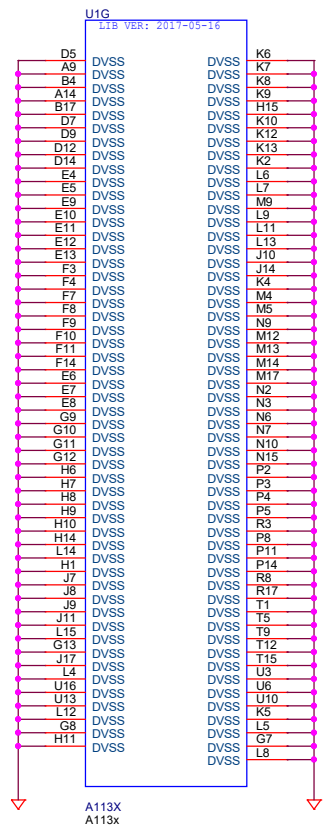
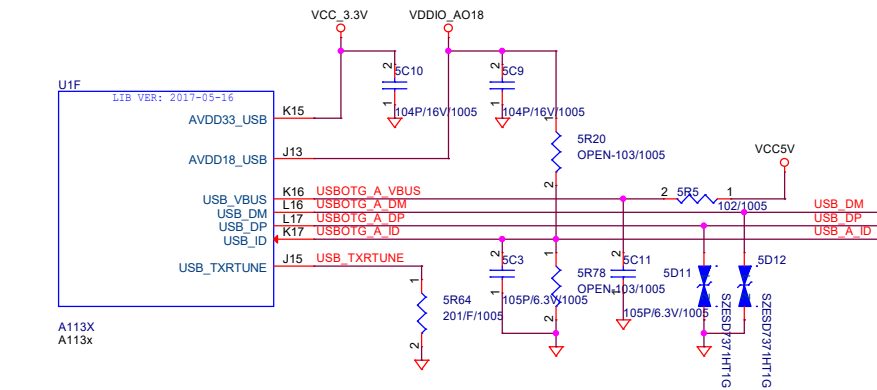


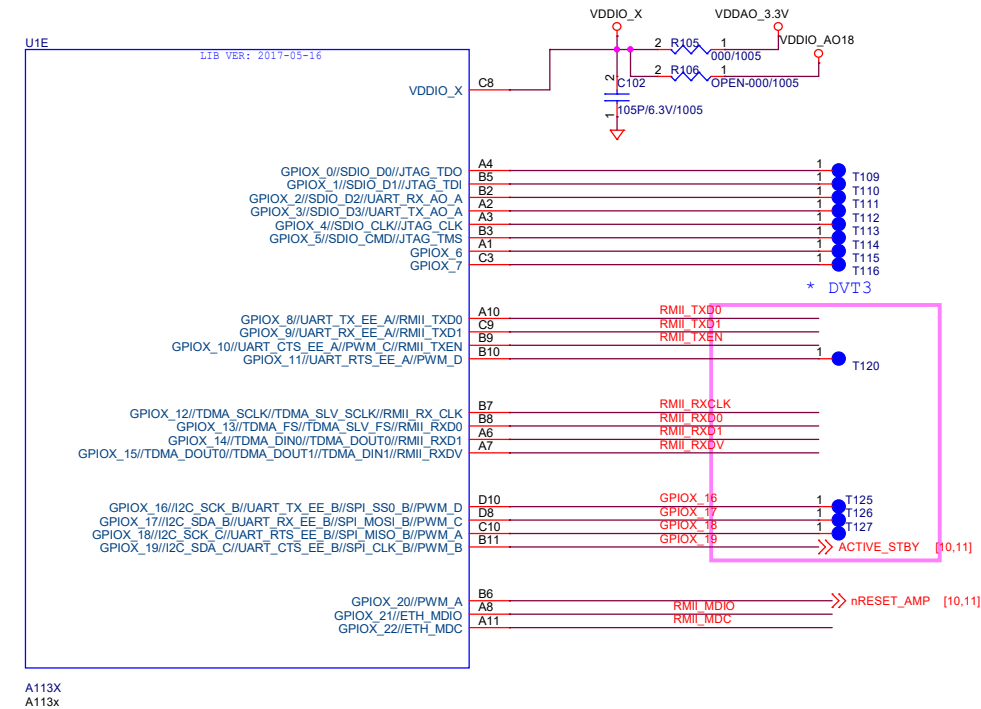
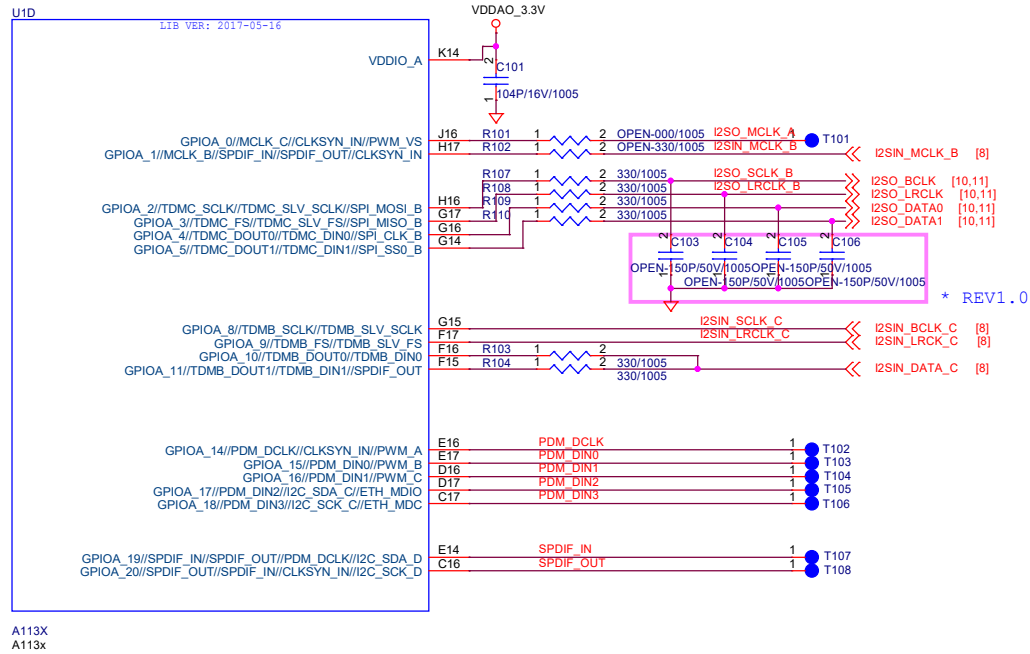
System LED



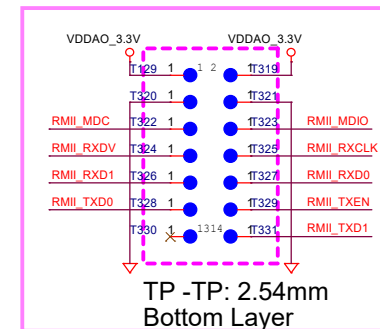
Logic	LED function
SYS_LED=1	System running: green LED







* DVT3



TP -TP: 2.5mm
Bottom Layer

