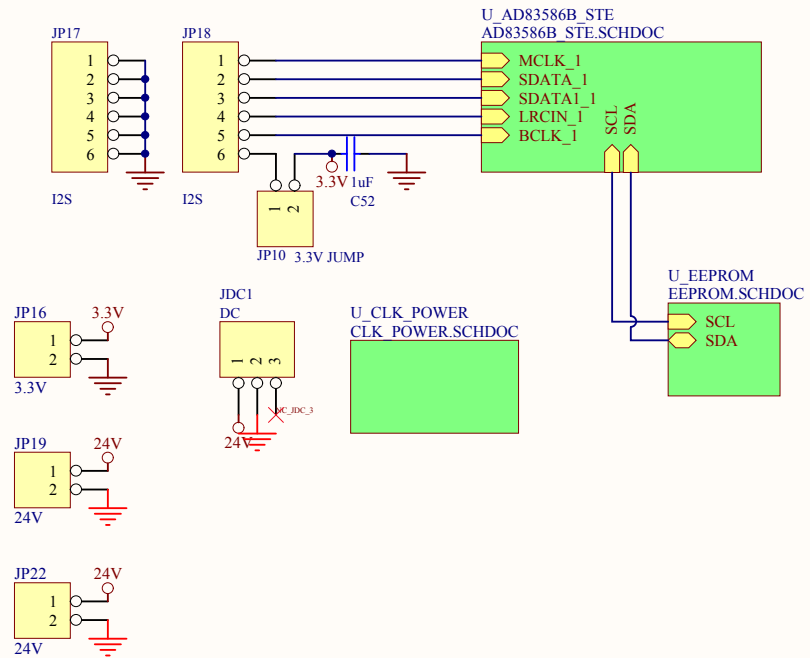
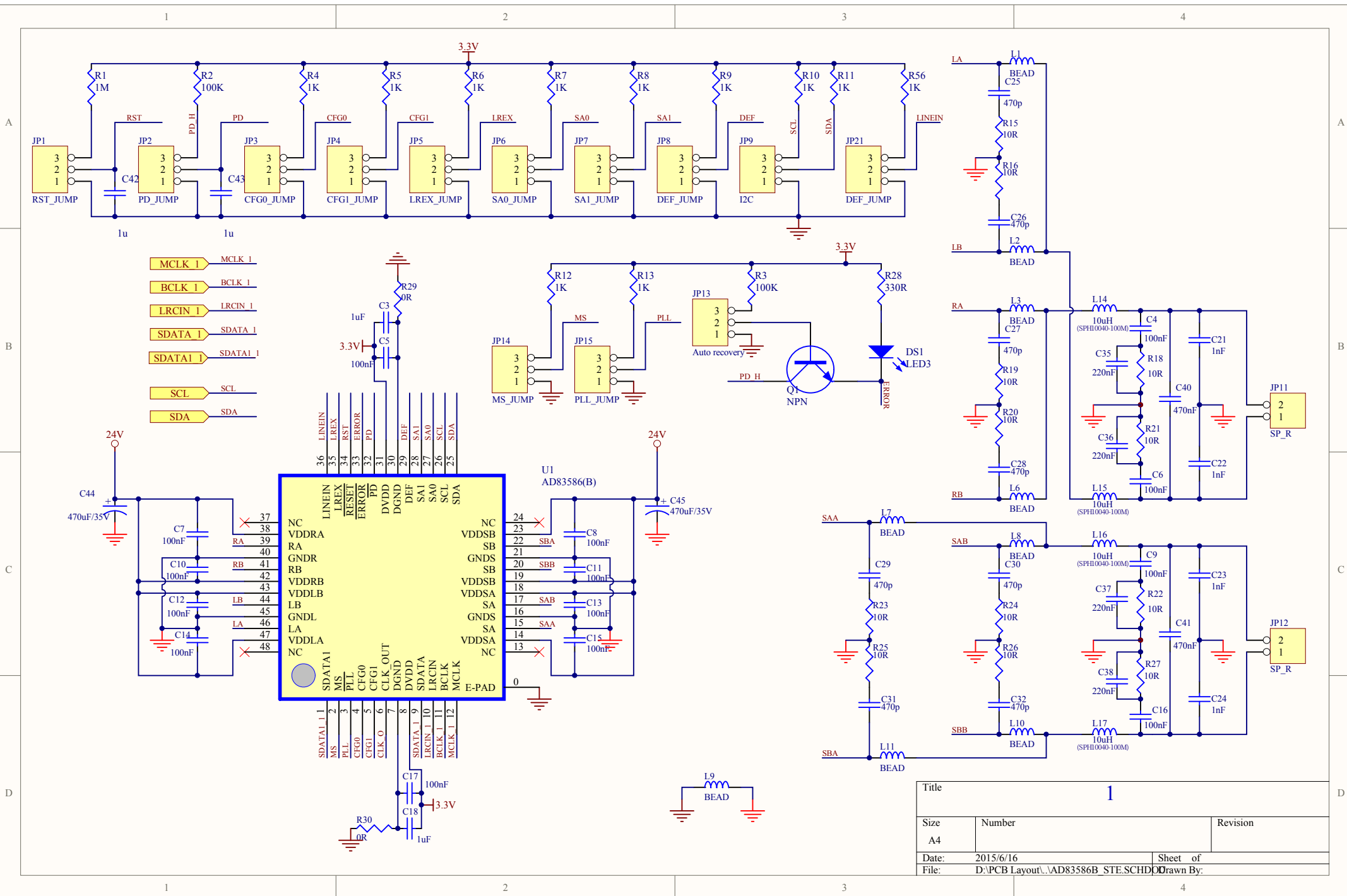
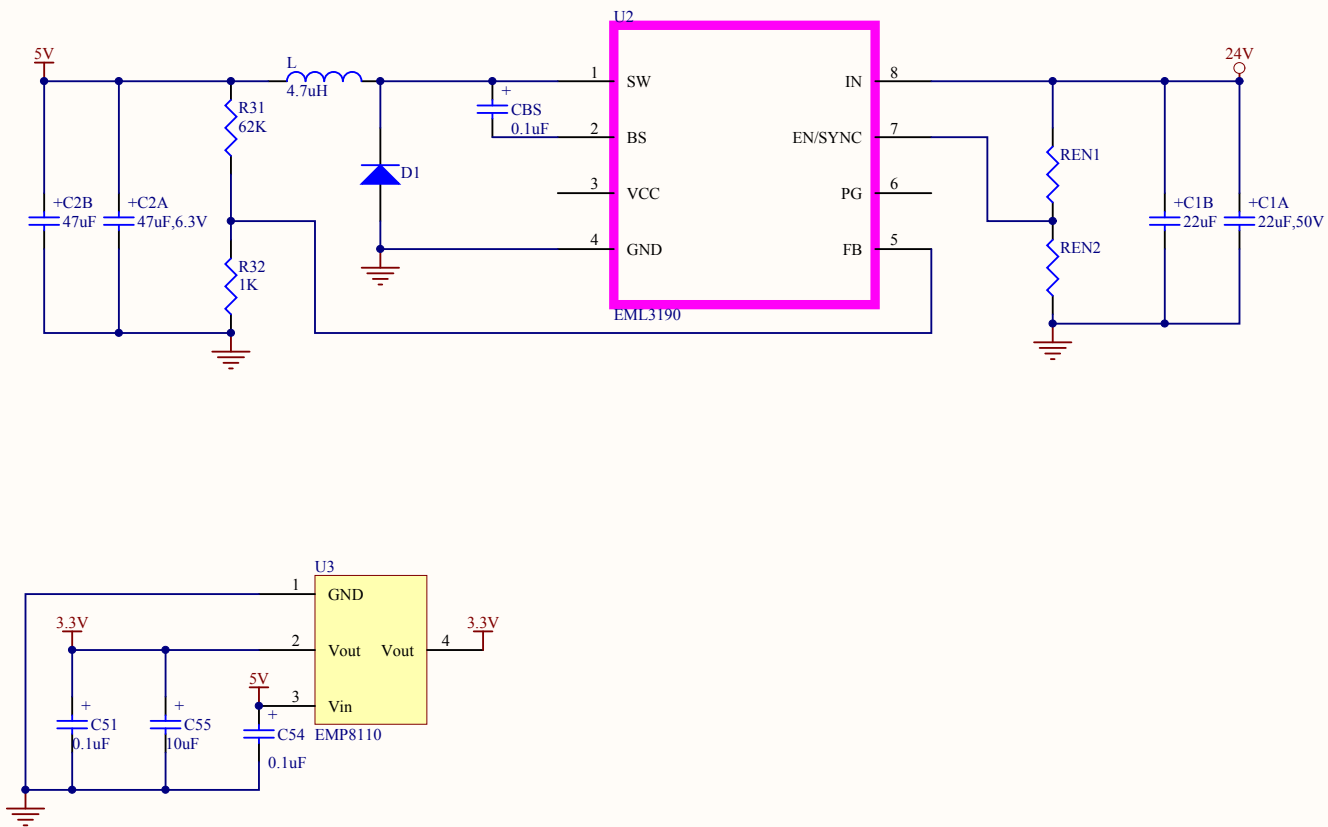


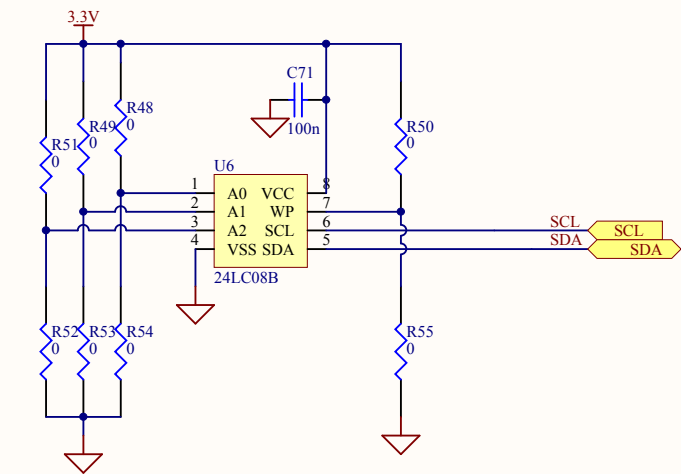


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Write Protect(WP): VCC-->write protected.
VSS-->normal memory operation is enabled.

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